

H Tree 4-Spines Clock Network

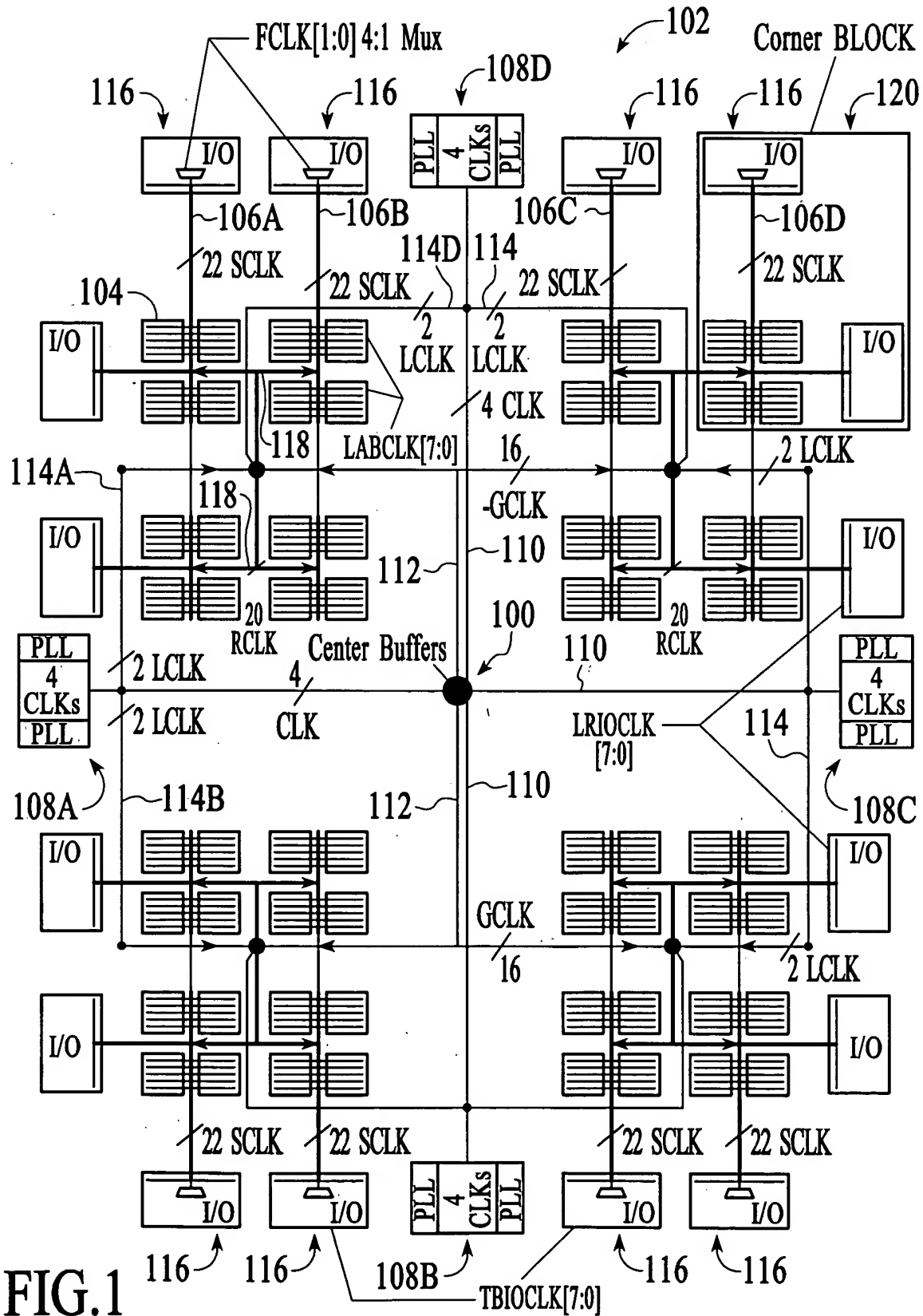


FIG.1

H Tree 2-Spines Clock Network

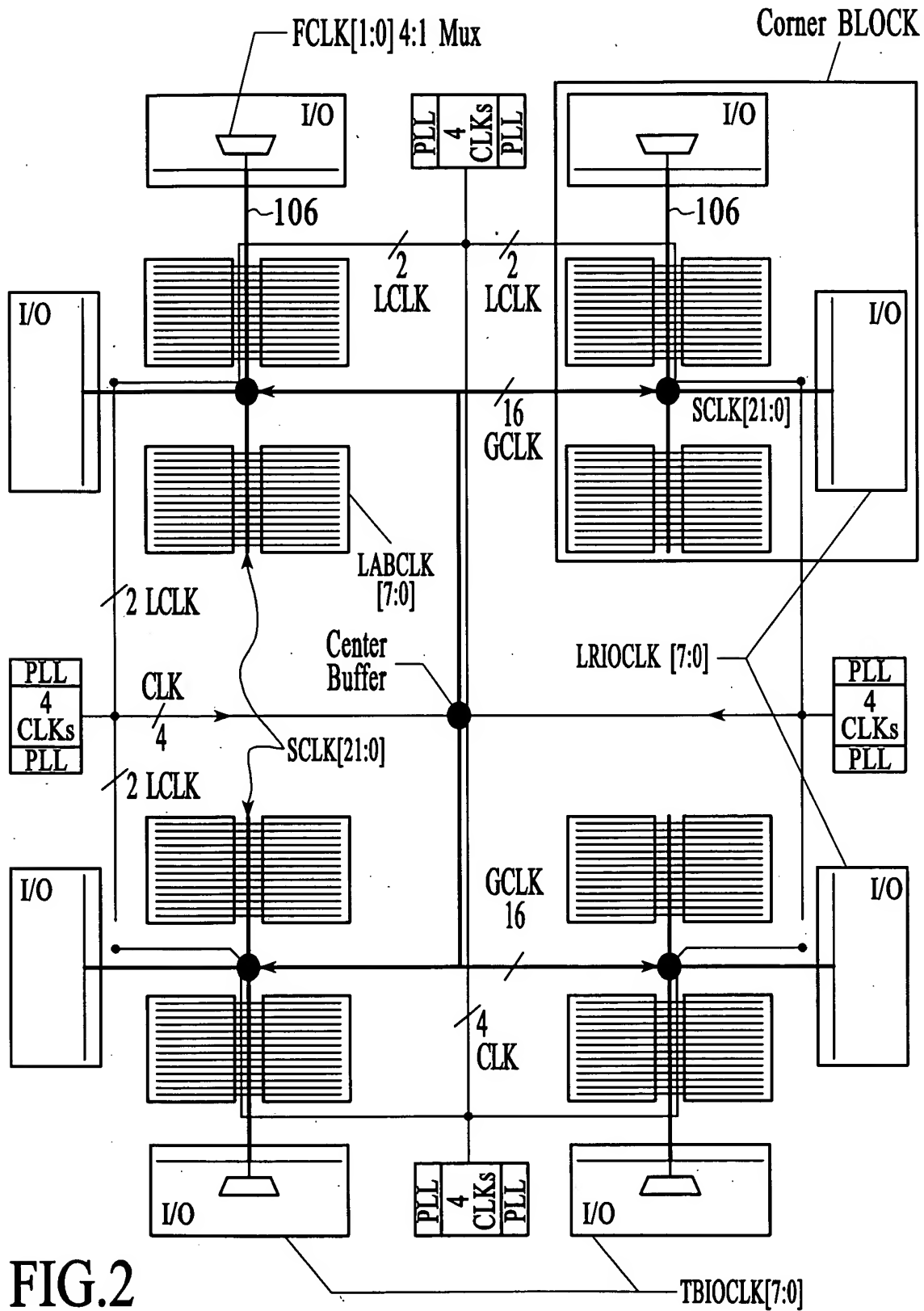


FIG.2

CORNER BLOCK for 4-Splines Clock Network

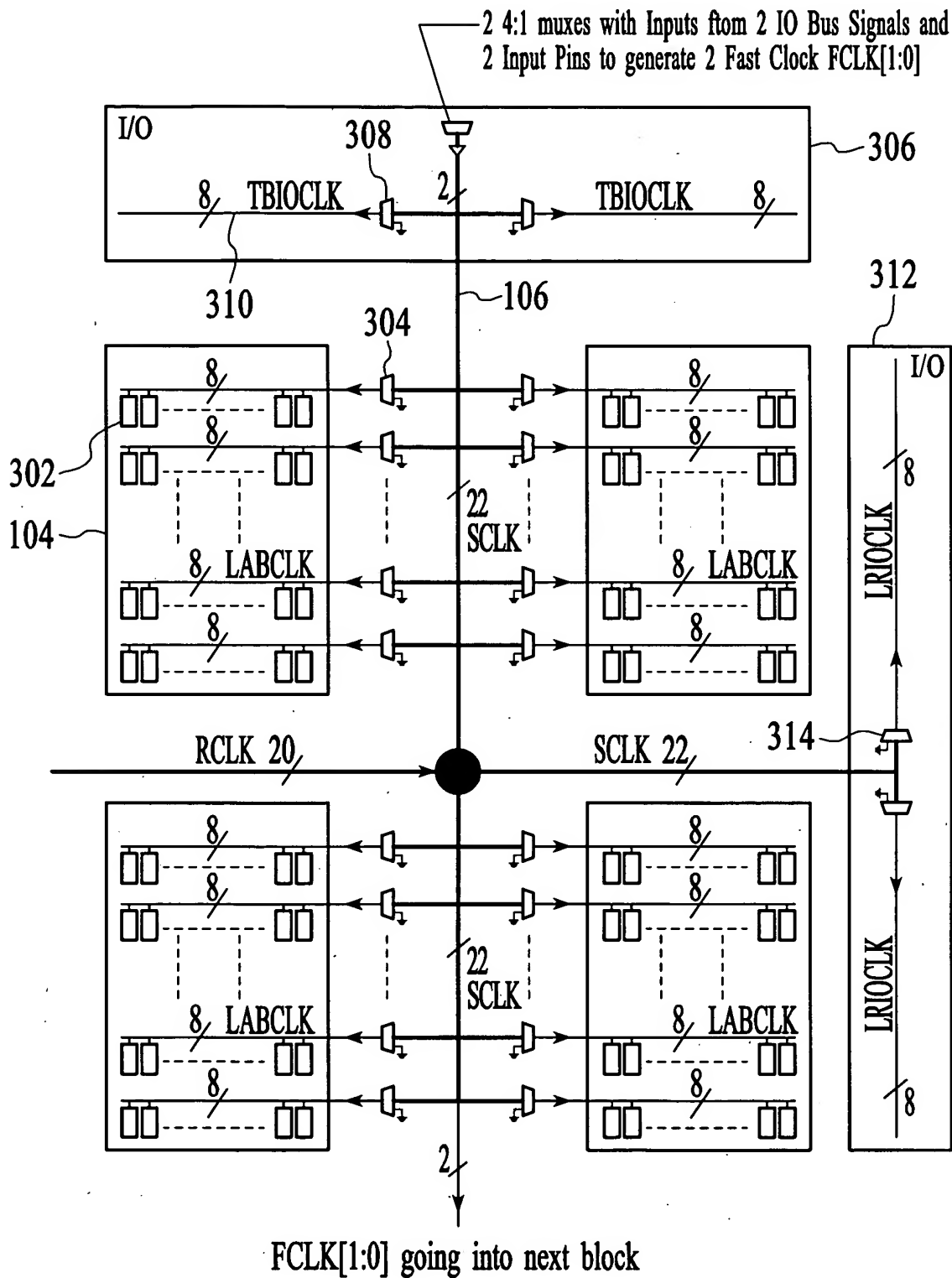


FIG.3

CORNER BLOCK for 2-Splines Clock Network

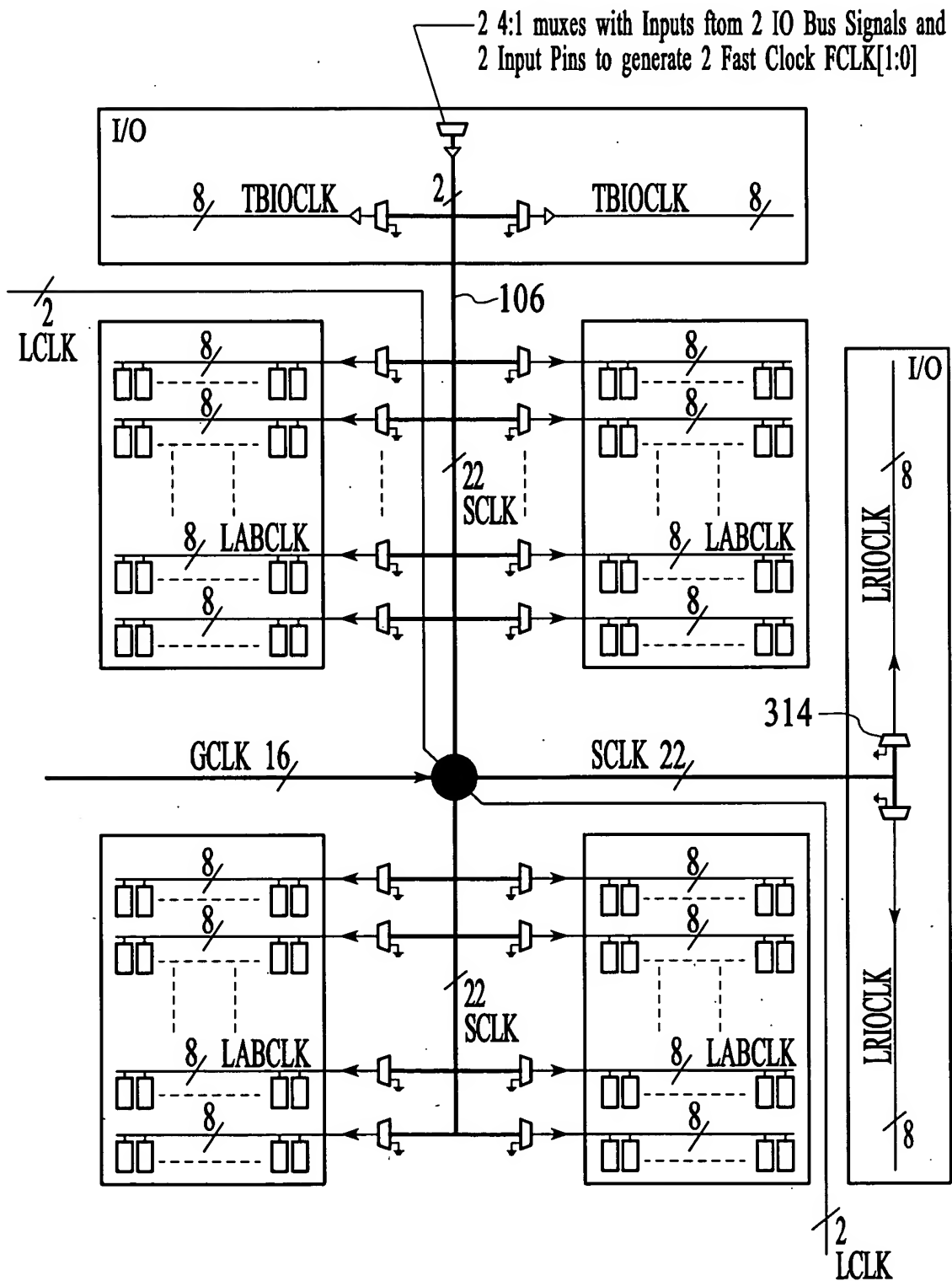
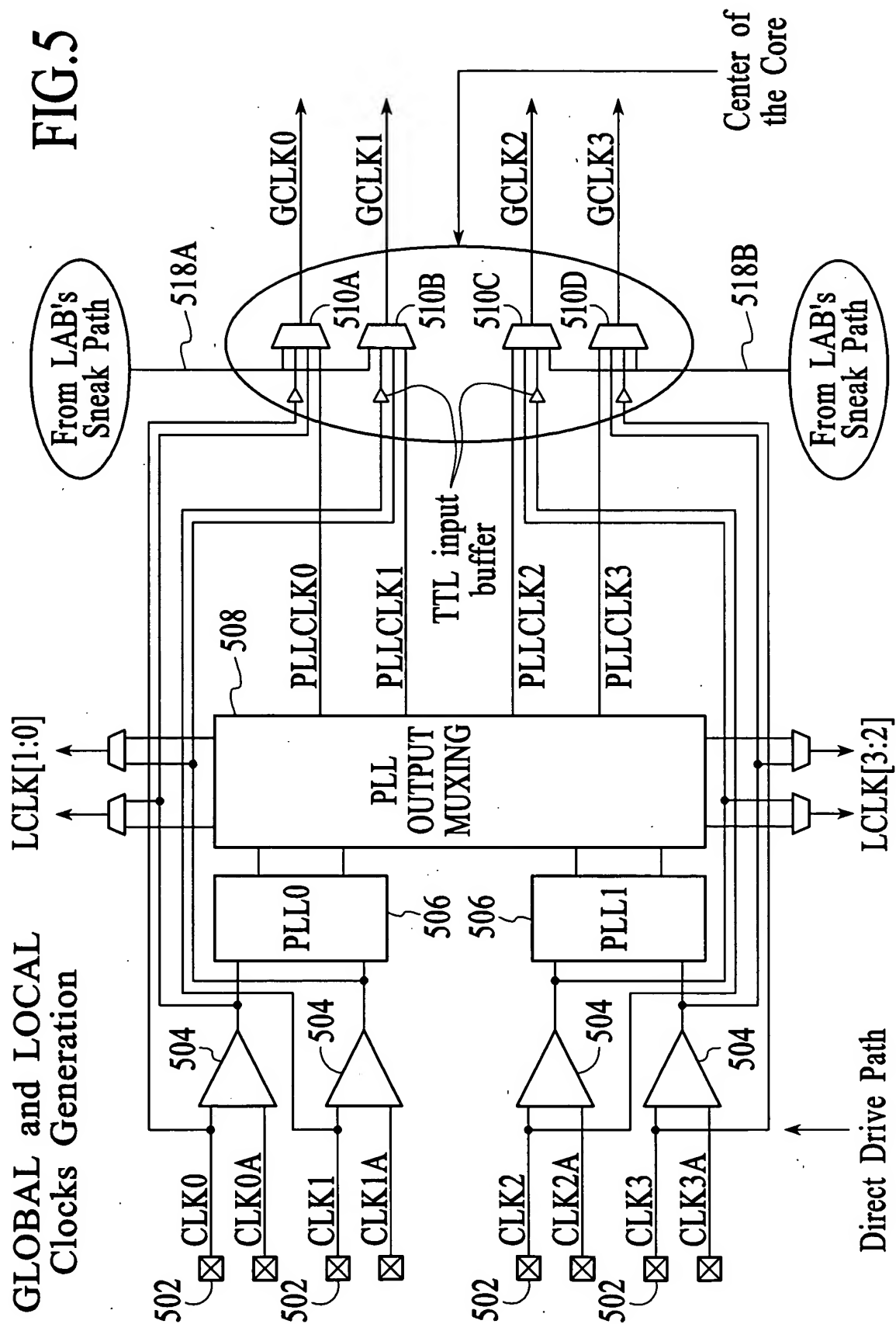


FIG.4



Generation of Global Clocks GCLK[15:0]

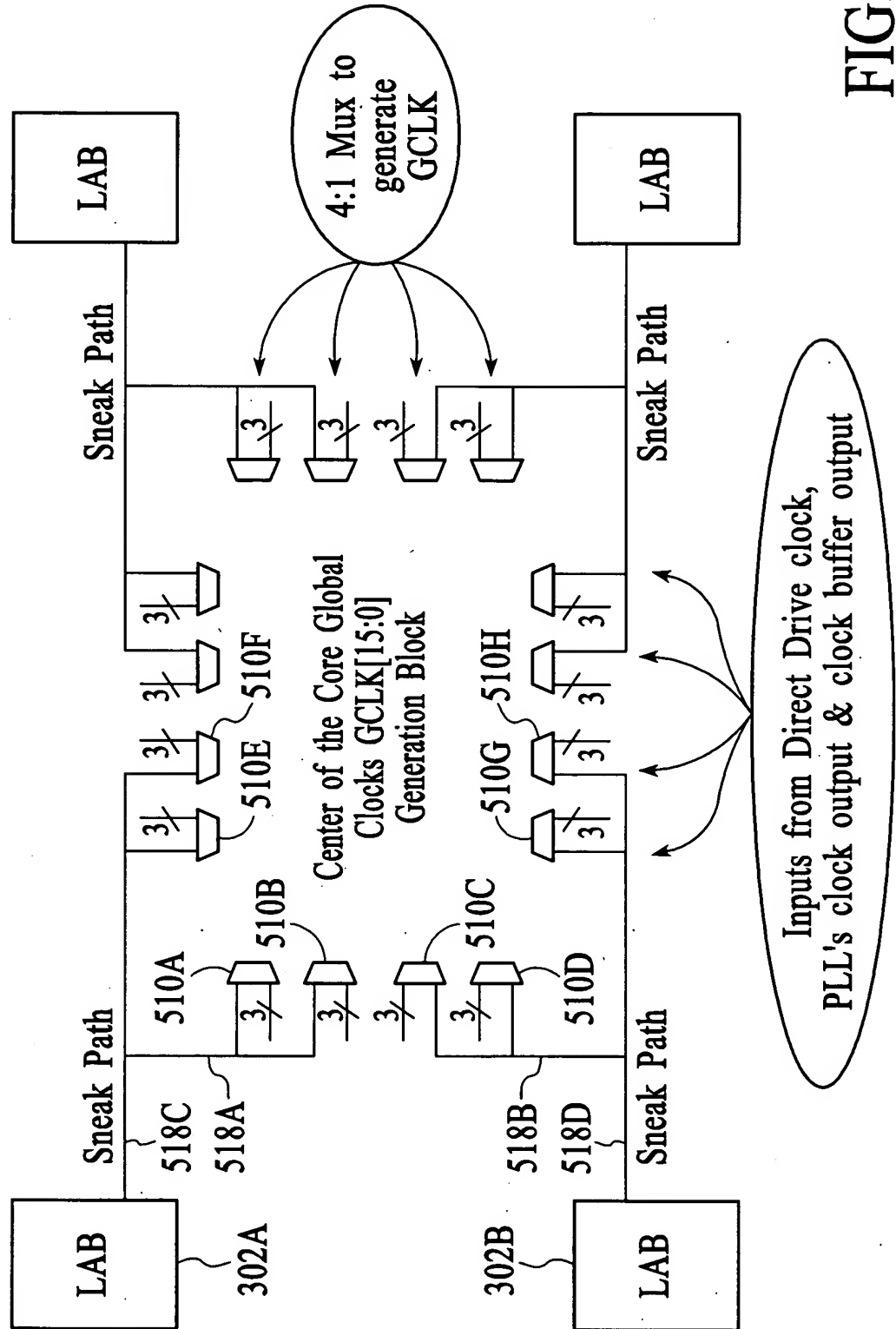


FIG.6

Power Bus Segmentation for CLOCKS

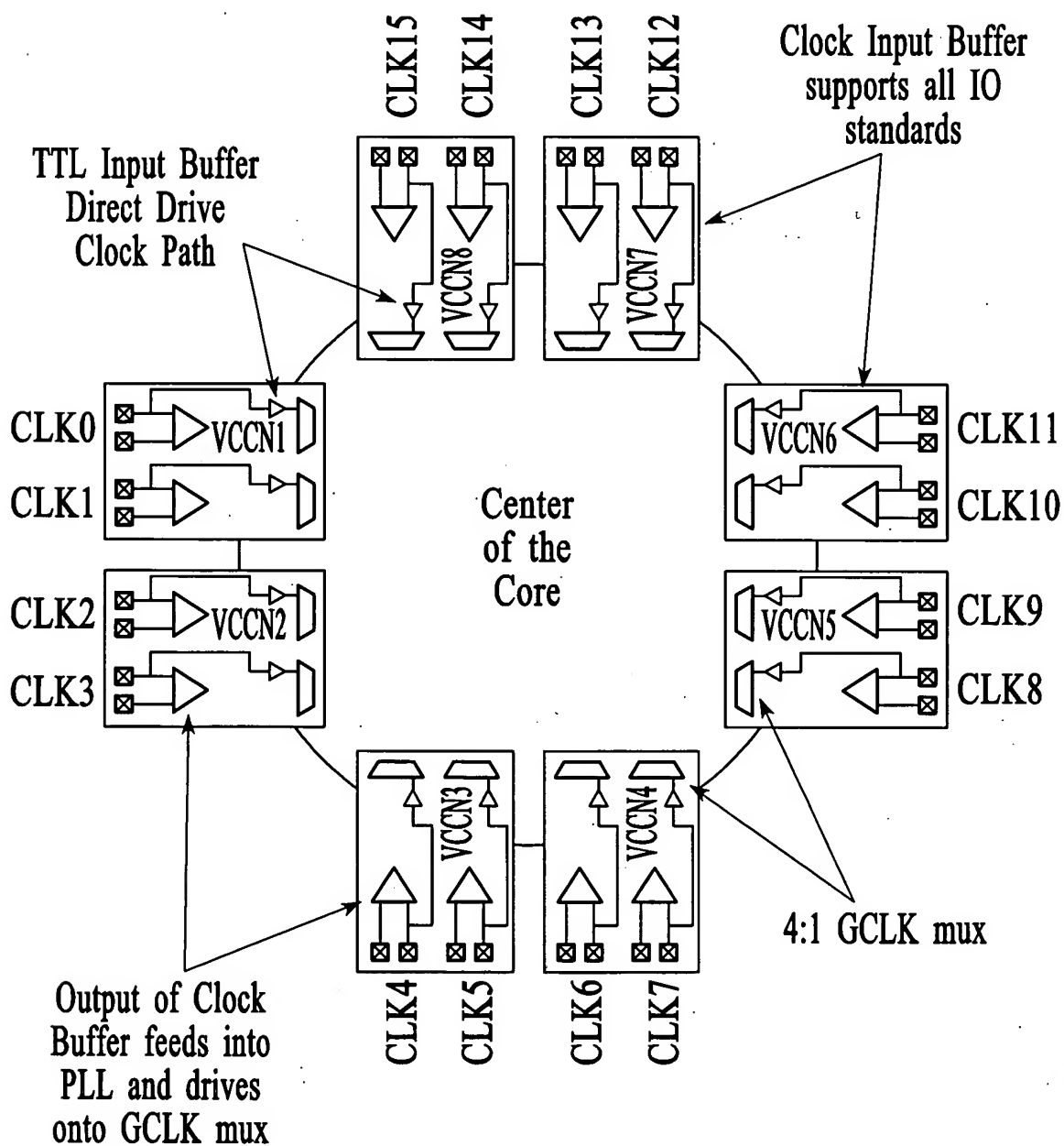


FIG.7